

Electronic Acknowledgement Receipt

EFS ID:	5903027
Application Number:	10620045
International Application Number:	
Confirmation Number:	7529
Title of Invention:	Method and apparatus for automatic generation of multiple integrated circuit simulation configuration
First Named Inventor/Applicant Name:	Rajarshi Bhattacharya
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		1150-1145_AM3.pdf	767905	yes	9
			033ce7e1507f4118db4422c918862f9eeb4f2e9d		
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment/Argument after BPAI Decision		1	1	
	Claims		2	6	
	Amendment/Argument after BPAI Decision		7	9	
Warnings:					
Information:					
2	Request for Continued Examination (RCE)	1150-1145_RCE.pdf	156620	no	2
			ca34b6320abc08ddd6628518a2562faa3e9c3db1		
Warnings:					
This is not a USPTO supplied RCE SB30 form.					
Information:					
3	Fee Worksheet (PTO-875)	fee-info.pdf	29683	no	2
			c05bd1c27b4256cb7a02e4062270c6fd6ac95de7		
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New International Application Filed with the USPTO as a Receiving Office

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